

Figure 1A (Prior Art)

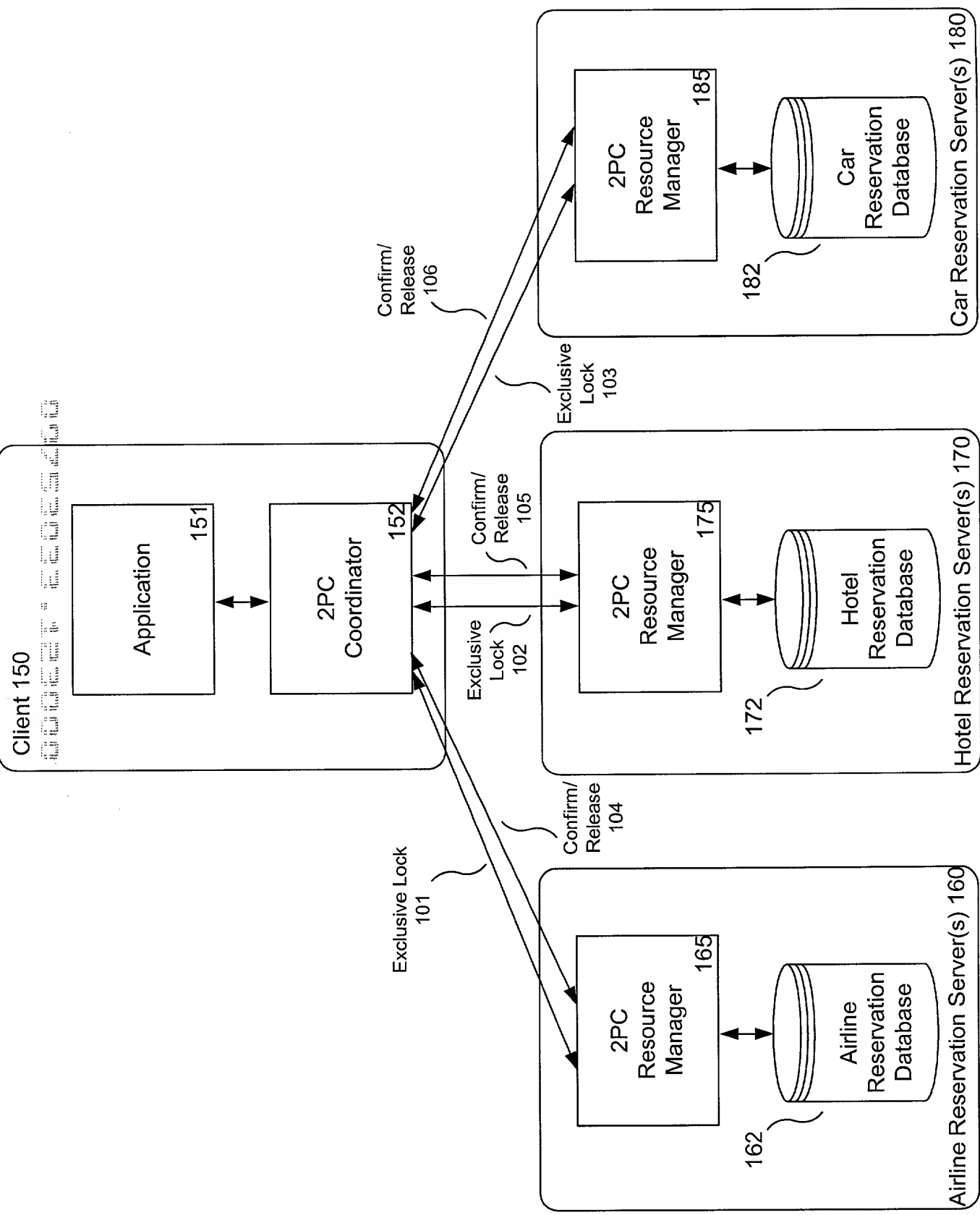
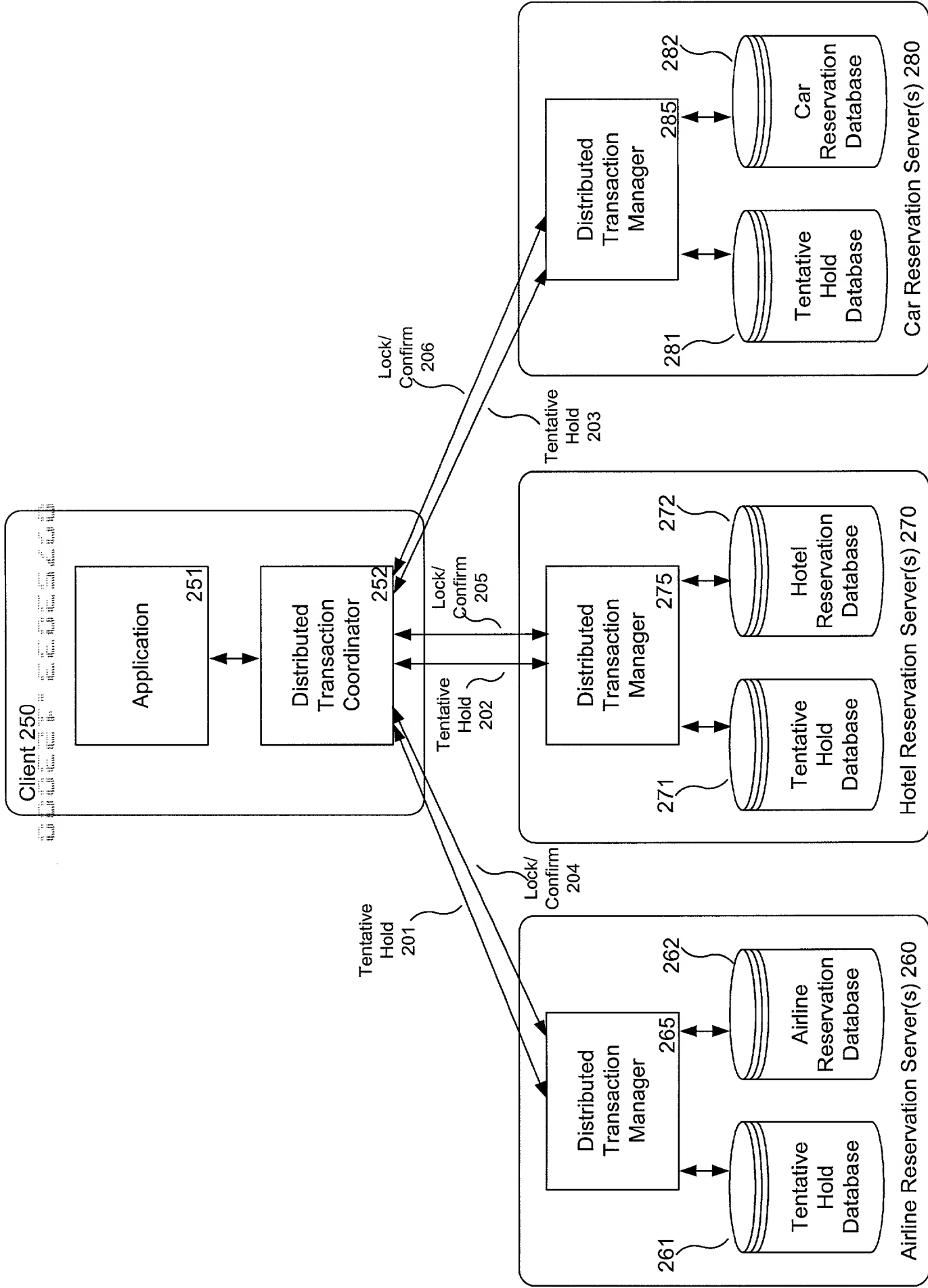


Figure 1B



**Figure 2**

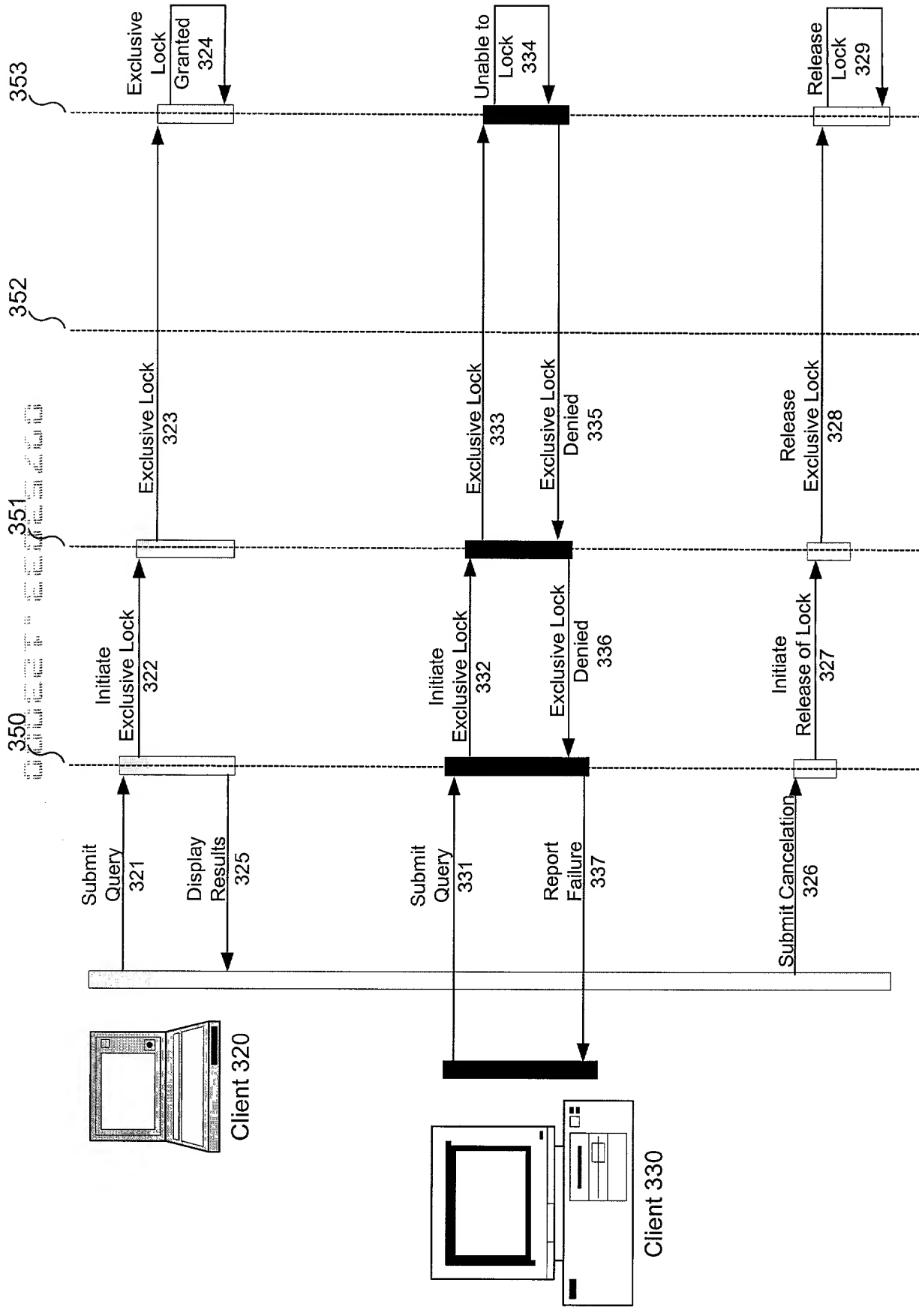


Figure 3A

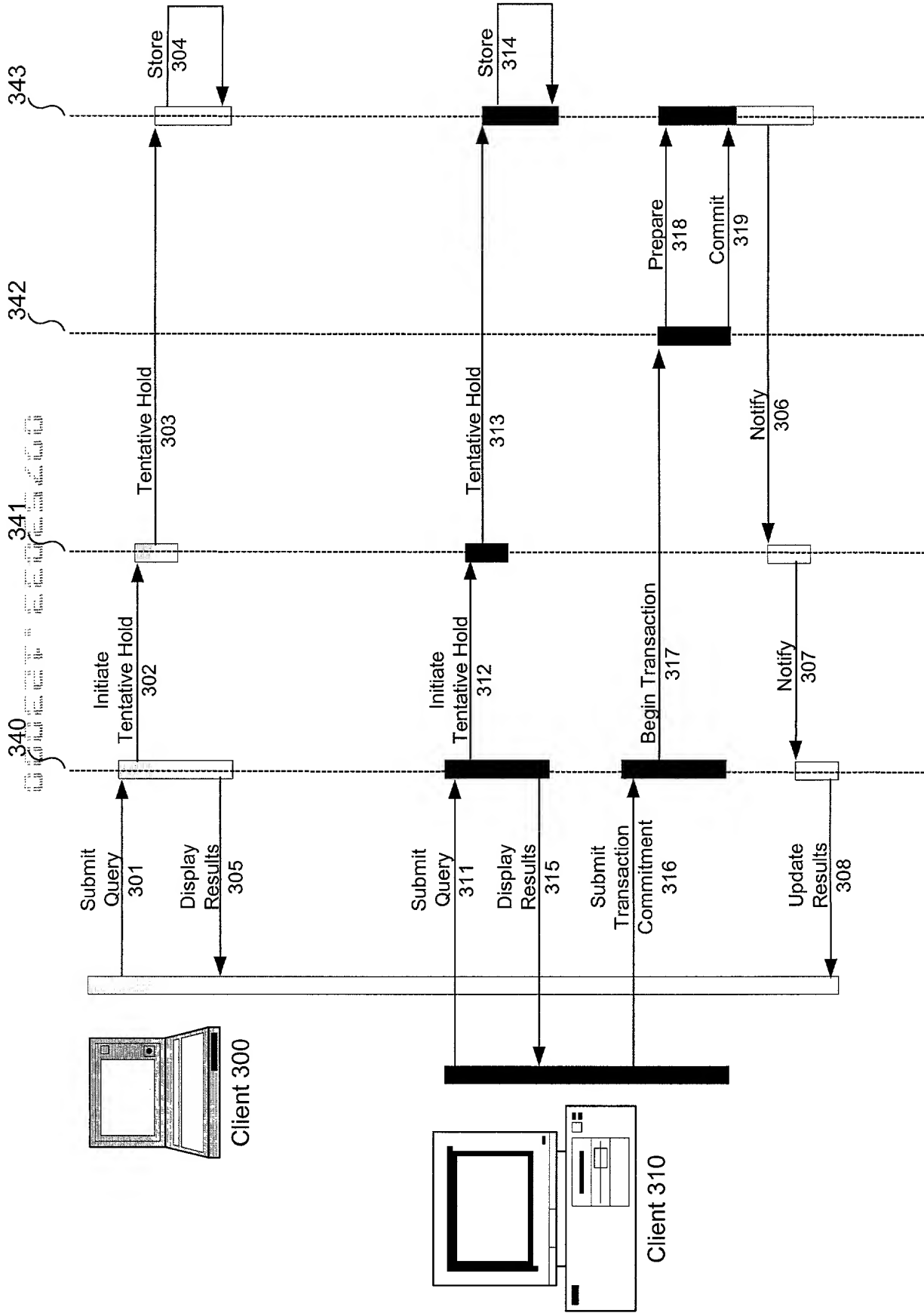


Figure 3B

FIG. 4 is a block diagram of a computer system 400.

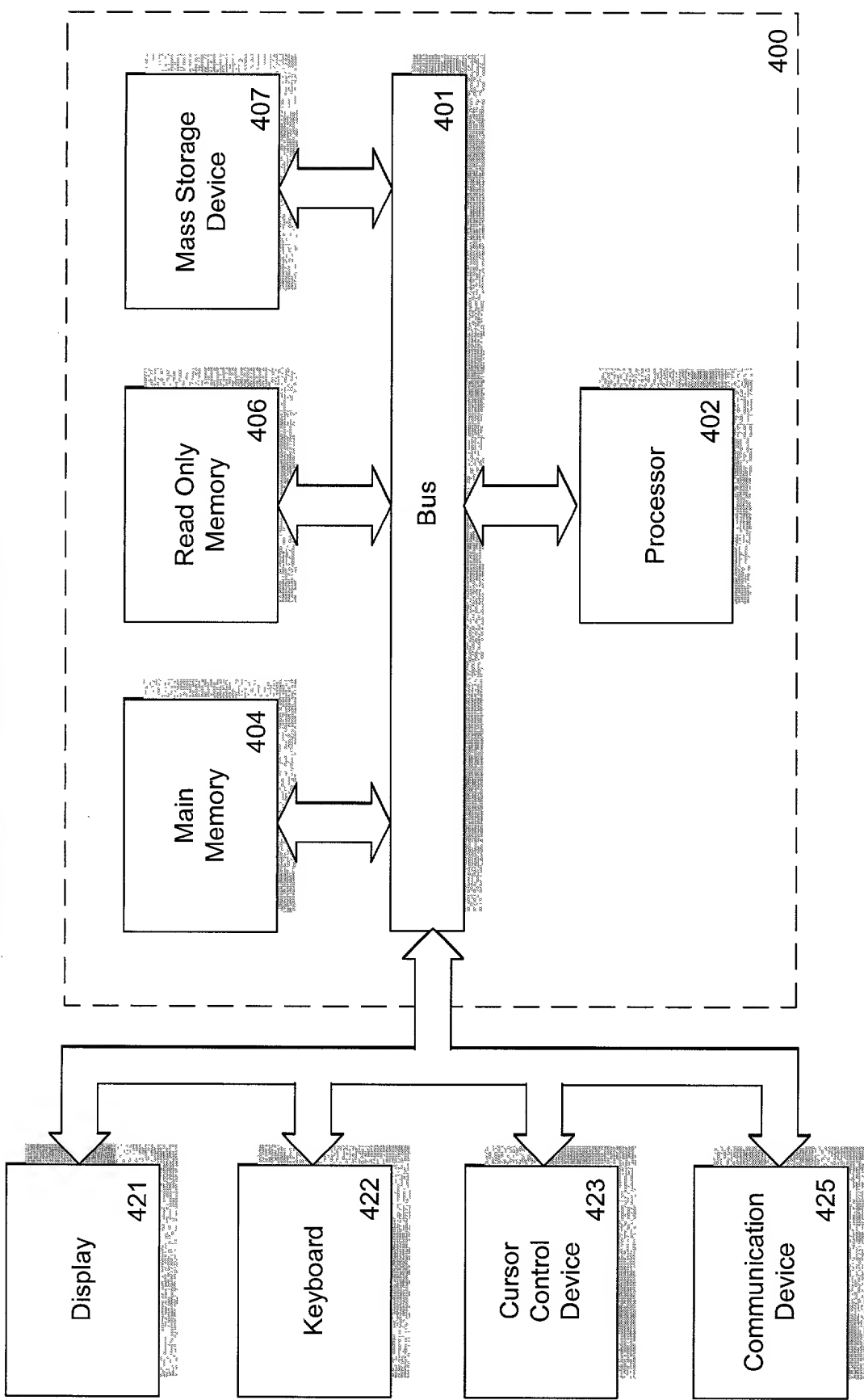
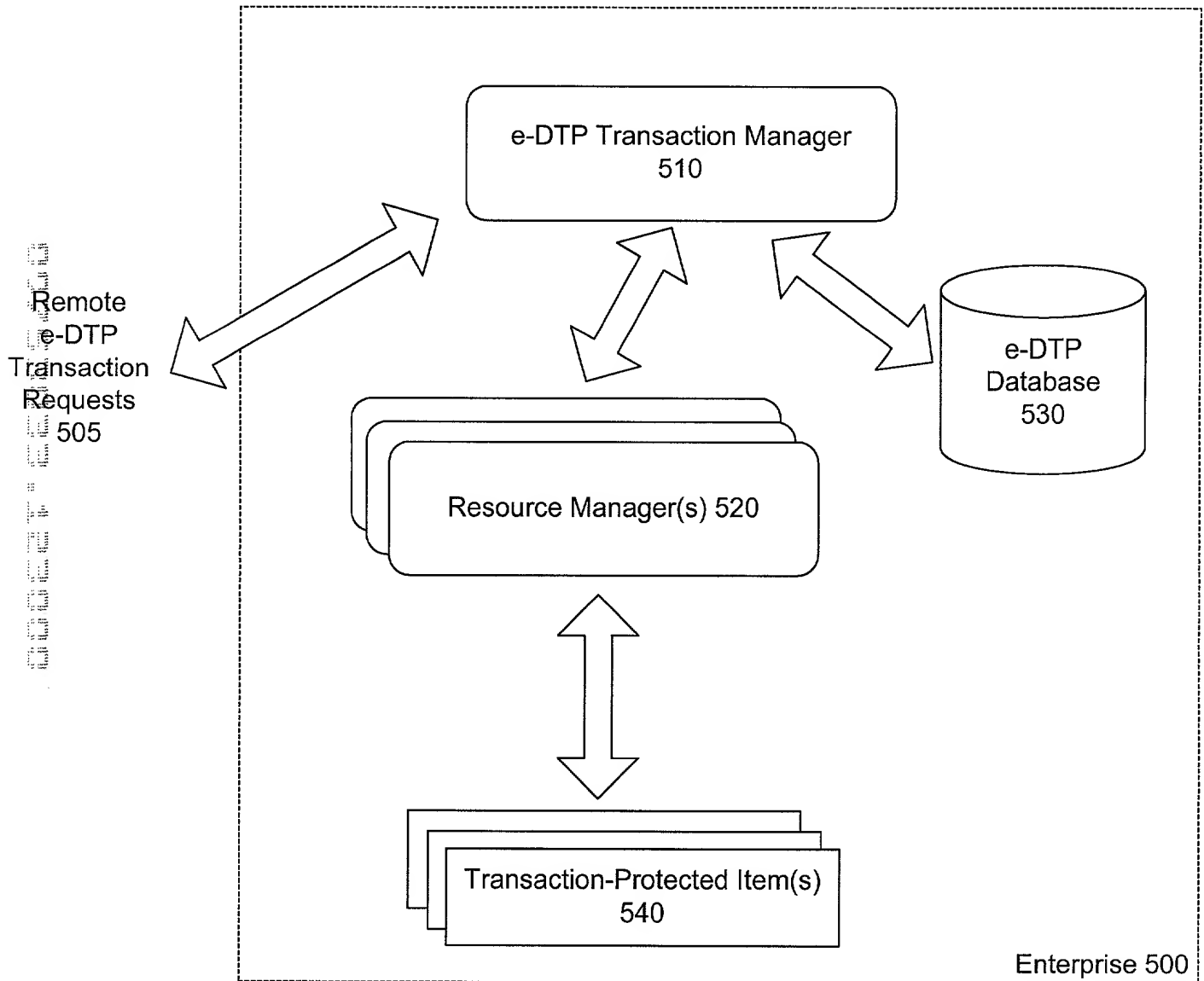
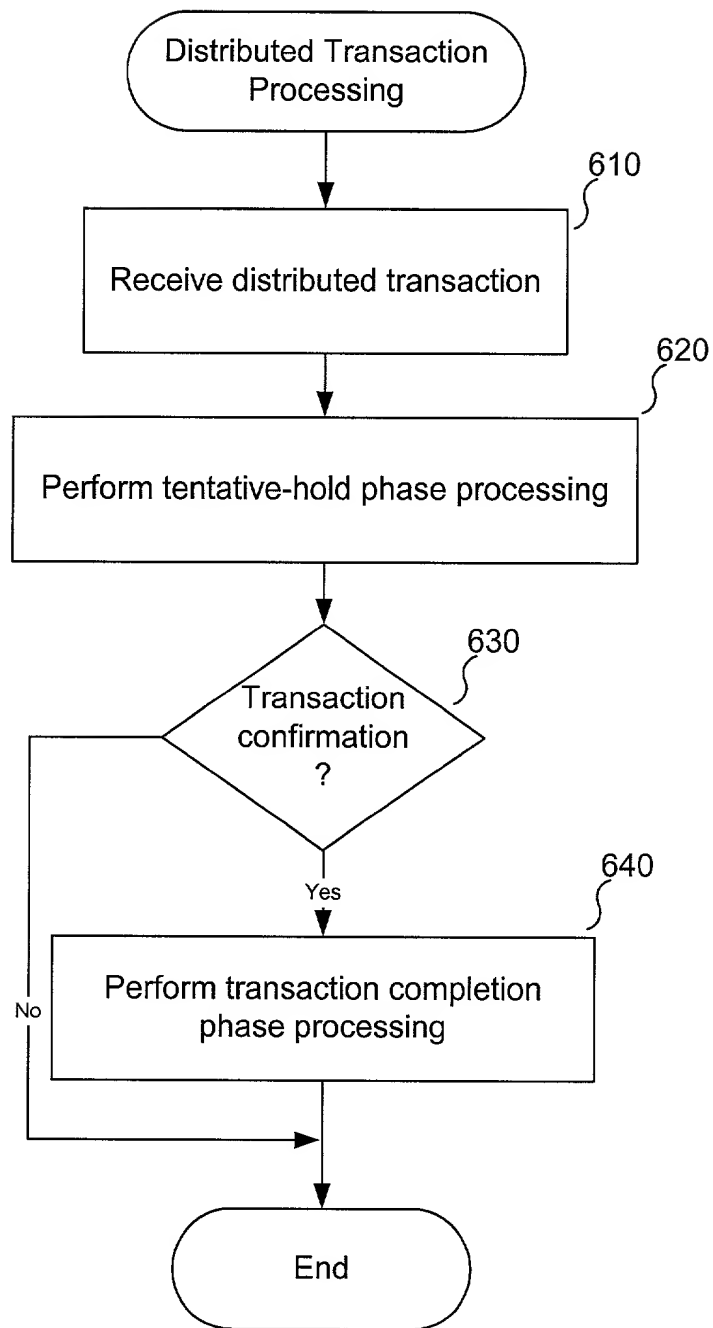


Figure 4

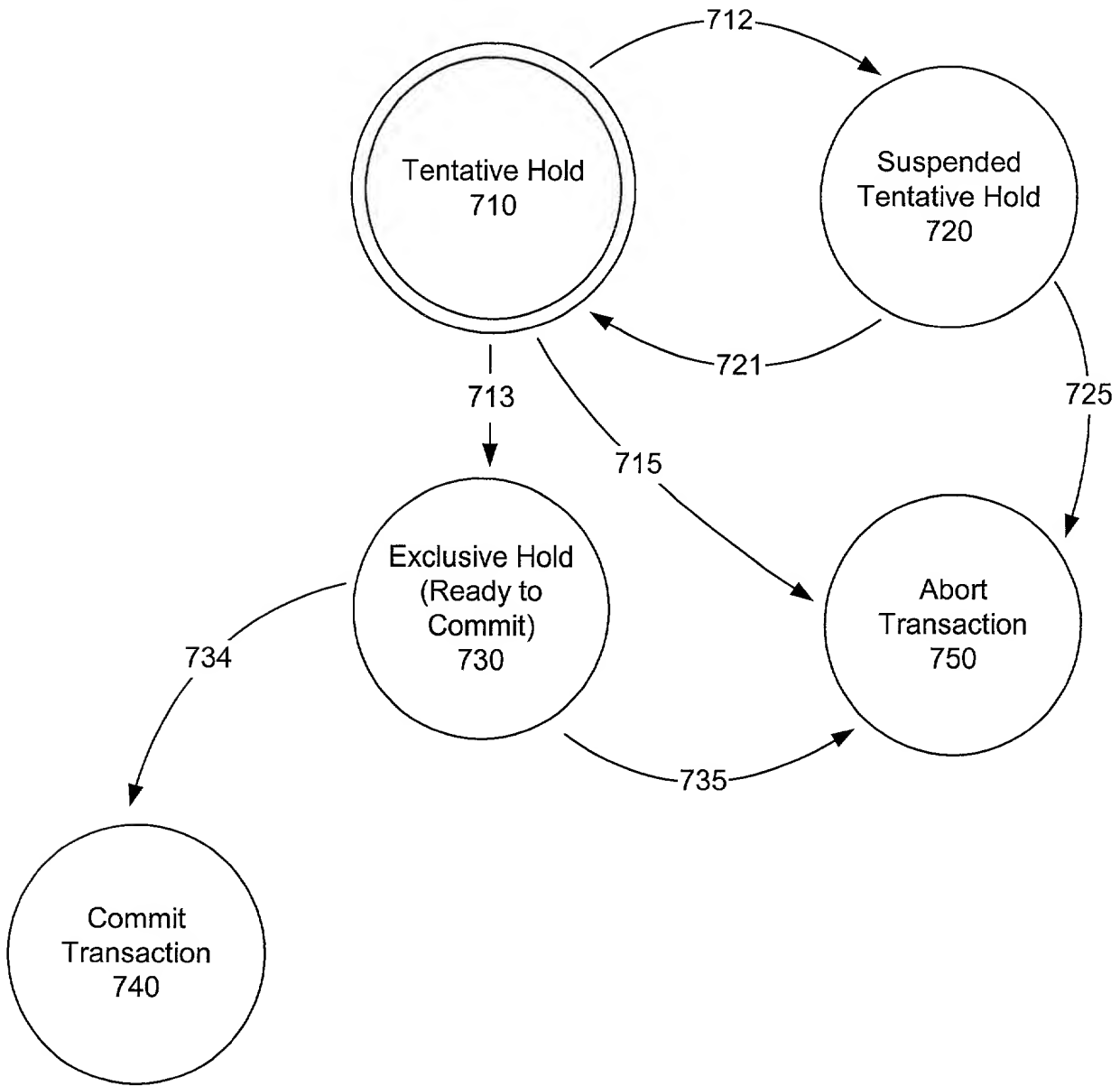


**Figure 5**



**Figure 6**





**Figure 7**